

**TRIBHUVAN UNIVERSITY**

**Faculty of Management**

**Shanker Dev Campus**

MICROPROCESSOR AND COMPUTER ARCHITECTURE

A LAB REPORT

Submitted To

Department of BIM  
Shanker Dev Campus  
Putalisadak, Kathmandu

*In partial fulfillment of the requirements for the Third Semester of the Bachelor of Information Management Degree*

Submitted By

ASHESH NEUPANE  
(TU Exam Roll No: 16295/23)

Under Supervision: **Mr. Narayan GC**

*March 2025*

**Supervisor’s Signature:**